

34. The device of claim 31 wherein said image data processing means includes ROM means for storing gradated display data [a ROM table].

35. The device of claim 34 wherein said data signal supply means includes counter means for forming pulses to drive the electro-optical display and flip-flop means operably connected with said ROM means, said counter means and a clock line for controlling the operation of said counter means [a flip-flop circuit and a counter].

Please add new claims 38-40 as follows:

--38. The driving method of claim 21 wherein each of said pixels contains a pixel electrode adjacent to said light modulating layer, a thin film transistor connected to said pixel electrode at one of its source or drain, wherein said thin film transistor has the other one of its source or drain addressed with said scan signal, and a gate supplied with said data signal.

39. The driving method of claim 26 wherein each of said pixels contains a pixel electrode adjacent to said light modulating layer, a thin film transistor connected to said pixel electrode at one of its source or drain, wherein said thin film transistor has the other one of its source or drain addressed with said scan signal, and a gate supplied with said data signal.

40. The driving method of claim 31 wherein each of said pixels contains a pixel electrode adjacent to said light modulating layer, a thin film transistor connected to said pixel electrode at one of its source or drain,